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IEC 62132-1 - Integrated circuits – Measurement of electromagnetic immunity – Part 1: General conditions and definitions Published by IEC on October 1, 2015 This part of IEC 62132 provides general information and definitions about measurement of electromagnetic immunity of integrated circuits (ICs) to conducted and radiated disturbances.

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International Standard IEC 62132-1 has been prepared by subcommittee 47A: Integrated circuits, of IEC technical committee 47: Semiconductor devices. This second edition cancels and replaces the first edition published in 2006 and constitutes a technical revision.

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